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Sir:

Transmitted herewith for filing is the patent application of:

INVENTOR(S): TSUTOMU YAMADA

FOR: COLOR DISPLAY DEVICE

Enclosed are:

☒ 23 pages of specification.☒ 18 claims.☒ 9 sheet(s) of drawing(s).☒ Declaration and Power of Attorney☒ Information Disclosure Statement.☐ An associate power of attorney.☒ An assignment of the invention to Sanyo Electric Co., Ltd.☐ A certified copy of an _____ application.☐ A verified statement to establish small entity under 37 CFR 1.9 and 37 CFR 1.27.

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INDEP. CLAIMS (3)	3	0	x 39		x 78	
MULTIPLE DEPENDENT CLAIMS	0	0	+130		+260	
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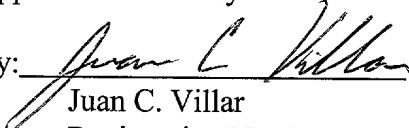
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Please file this application and conduct all future correspondence with Applicant's attorney identified below.

Respectfully Submitted,

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Date: October 4, 2000

COLOR DISPLAY DEVICE

BACKGROUND OF THE INVENTION

1. Field of the Invention

5 The present invention relates to a display device using a self-emissive element such as an electroluminescence (hereinafter referred to as "EL") element and a thin film transistor (hereinafter referred to as "TFT").

2. Description of the Related Art

10 In the recent years, display devices using EL elements, which are self-emissive elements, have gained attention as the display devices that may replace CRTs and LCDs.

Research is being directed to development of display devices using TFTs as the switching elements for driving the EL elements.

15 Fig. 2 is an equivalent circuit diagram of a conventional organic EL display device. Fig. 3 is a plan view of the organic EL display device showing an area around a display pixel. Fig. 4A is a cross-sectional view taken along line A-A of Fig. 3, while Fig. 4B shows a cross-sectional view taken along line B-B of Fig.

20 Fig. 5 illustrates the arrangement of display pixels in the organic EL display device.

As shown in Figs. 2 and 3, display pixels 110 are formed in regions surrounded by gate signal lines 51 and drain signal lines 52. The display pixels 110 are arranged in a matrix. A first TFT 30, which is a switching TFT, is provided in an area near an intersection of the two signal lines 51, 52. The source 13s of the TFT 30 concurrently serves as a capacitor electrode 55 which

functions together with a storage capacitor electrode line 54 described below to create capacitance. The source 13s is connected to the gate 41 of a second TFT 40 which is an EL element driving TFT. The source 43s of the second TFT 40 is connected to an anode 5 61 of an organic EL element 60. The drain 43d of the TFT 40 is connected to the power source line 53 which supplies a current to the organic EL element 60.

The storage capacitor electrode line 54 is arranged in parallel to gate signal lines 51. The storage capacitor electrode line 54 is made of a material such as chrome, and forms a storage capacitor 56 by allowing charges to be accumulated between the capacitor electrode 55 connected to the source 13s of the TFT 30 and the storage capacitor electrode line 54, while a gate insulating film 12 is disposed therebetween. The storage capacitor 56 is provided for retaining a voltage applied to the gate electrode 41 of the second TFT 40.

As shown in Figs. 4A and 4B, the organic EL display device is formed by sequentially laminating TFTs and an organic EL element on a substrate 10 made of an insulating material such as glass or synthetic resin, or, alternatively, a conductive or semiconductor material. When a conductive or semiconductor substrate is used as the substrate 10, an insulating film such as SiO_2 or SiN is formed on the substrate 10 before forming the first and second TFTs and the organic EL display element.

The first TFT 30, which is the switching TFT, is first described.

As shown in Fig. 4A, sequentially formed on an insulating

substrate 10 made of a material such as quartz glass or non-alkali glass are an active layer 13 composed of a semiconductor film (p-Si film), a gate insulating film 12, and a gate signal line 51 made of a refractory metal such as chromium (Cr) or molybdenum (Mo).

5 The gate signal line 51 concurrently serves as gate electrodes 11. Further arranged are a drain signal line 52 composed of Al, and a power source line 53 made of Al and serving as the drive power source of the organic EL element.

10 An interlayer insulating film 15 composed through sequential lamination of a SiO₂ film, a SiN film, and a SiO₂ film is formed on the entire surface over the gate electrodes 11 and the gate insulating film 12. Subsequently, a drain electrode 16 is formed by filling a metal such as Al in a contact hole created corresponding to the drain region 13d. A planarizing insulating film 17 composed
15 of an organic resin is then formed over the entire surface for planarizing the surface.

The second TFT 40, which is the organic EL element driving TFT, is next described.

20 As shown in Fig. 4B, sequentially formed on an insulating substrate 10 made of a material such as quartz glass or non-alkali glass are an active layer 43 composed of a semiconductor film (p-Si film), a gate insulating film 12, and gate electrodes 41 made of a refractory metal such as Cr or Mo. The active layer 43 includes channels 43c, and source 43s and drain 43d on the outboard sides
25 of the channels. An interlayer insulating film 15 composed through sequential lamination of a SiO₂ film, a SiN film, and a SiO₂ film is formed on the entire surface over the gate insulating film 12 and the active layer 43. Subsequently, a contact hole is created

through the gate insulating film 12 and the interlayer insulating film 15 in a position corresponding to the drain region 43d. This contact hole is filled with a metal such as Al to connect the drain 43d with a power source line 53. The power source line 53 is
5 connected to a drive power source. A planarizing insulating film 17 composed of, for example, an organic resin is then formed over the entire surface for planarizing the surface. A contact hole is formed through the planarizing insulating film 17 in a position corresponding to the source 43s. A transparent electrode made of
10 ITO (indium tin oxide), namely, the anode 61 of the EL element 60, is formed on the planarizing insulating film 17 and through this contact hole to connect with the source 43s.

The organic EL element 60 is configured by first providing the anode 61 made of a transparent electrode composed of a material
15 such as ITO. The emissive element layer 66 is then superimposed. The emissive element layer 66 comprises a first hole-transport layer 62 composed of MTDATA or a similar material, a second hole-transport layer 63 composed of a material such as TPD, an emissive layer 64 composed of, for example, Bebq₂ including
20 quinacridone derivatives, and an electron transport layer 65 composed of a material such as Bebq₂. Subsequently, the cathode 67 is formed, for example, by using an alloy of magnesium (Mg) and silver (Ag), or by laminating lithium fluoride (LiF) and Al. All of the above-mentioned layers constituting the organic EL element
25 60 are laminated in the described order. By selecting materials which emit light of predetermined colors as the emissive materials of the emissive element layer 66, display pixels can be configured to emit light of different colors. The organic EL display device

is configured by arranging the display pixels of respective colors in a matrix layout.

In the organic EL element, holes injected from the anode and electrons injected from the cathode recombine in the emissive layer.

5 As a result, organic molecules constituting the emissive layer are excited, generating excitons. Through the process in which these excitons undergo radiation until deactivation, light is emitted from the emissive layer. This light radiates outward through the transparent anode via the transparent insulating substrate,
10 resulting in light emission.

As described above, in each display pixel 110 emitting a color, an EL element driving TFT 40 is connected for driving the organic EL element of that pixel. Fig. 3A is an enlarged view of a driving TFT 40. The transistor size of the TFT 40, namely, ratio W/L
15 concerning the channel width W and the channel length L of the channel region in which the semiconductor film of the TFT 40 and the gate electrodes overlap (in the case of Fig. 3A, $L=L_1+L_2$) is identical in all TFTs 40.

Furthermore, emissive efficiency of the emissive layer in each
20 display pixel differs according to the emitted color depending on the organic emissive material constituting the emissive layer.

In such an arrangement, to supply different values of current to the organic EL elements for different colors according to the emissive efficiency of each color so as to obtain the same level
25 of luminance for the respective colors and establish an appropriate white balance, it is necessary to alter the current value of the power source for each color, or to alter the potential of the drain signal supplied to the first TFT connected in each display pixel

according to each color. Specifically, more current must be made to flow in an organic EL element including an emissive layer of a color having a low emissive efficiency, compared to an organic EL element including an emissive layer of a color having a high
5 emissive efficiency.

However, conventional display devices are disadvantageous in that, to alter the current value of the power source for each color of the display pixels, the power source line must be arranged in a complex manner within the region in which the display pixels are
10 arranged. Further, to alter the potential of the drain signal supplied to the first TFT according to each color, complex circuitry is necessary for supplying a signal to the first TFT.

SUMMARY OF THE INVENTION

15 The object of the present invention is to provide an EL display device in which the sizes of the EL element driving TFTs in display pixels having EL elements for different colors are varied depending on emissive efficiency, such that the white balance among the display pixels for different colors can easily be controlled
20 without requiring complex circuitry.

A display device according to the present invention is a color display device comprising, in each display pixel, a self-emissive element and a self-emissive element driving thin film transistor (TFT) connected to and supplying a current to the self-emissive
25 element, wherein, among display pixels for different colors, transistor size of a self-emissive element driving TFT in a display pixel for any one color differs from that in a display pixel for

another color.

The transistor size is preferably determined according to emissive efficiency of the self-emissive element.

Further, the transistor size of a self-emissive element driving TFT connected to a self-emissive element having a high emissive efficiency is preferably set smaller compared to the transistor size of a self-emissive element driving TFT connected to a self-emissive element having an emissive efficiency lower than the above high emissive efficiency.

In addition, the transistor size of a self-emissive element driving TFT connected to a self-emissive element having the highest emissive efficiency is preferably set smaller compared to the transistor size of a self-emissive element driving TFT connected to a self-emissive element having any other level of emissive efficiency.

Furthermore, the self-emissive element preferably is an electroluminescence element.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a plan view of display pixels for different colors in an EL display device according to the present invention, in which TFT portions in the respective pixels are enlarged.

Figs. 1A, 1B, and 1C are diagrammatic illustrations showing examples in which the gate width W is varied while the gate length L is fixed.

Figs. 1D, 1E, and 1F are diagrammatic illustrations showing examples in which the gate length L is varied while the gate width

W is fixed.

Fig. 2 is a diagram showing an equivalent circuit according to the present invention and the related art.

Fig. 3 is a diagram showing a configuration of a display pixel according to the present invention and the related art.

Fig. 3A shows a TFT in a display pixel.

Fig. 4A shows cross-section A-A of Fig. 3.

Fig. 4B shows cross-section B-B of Fig. 3.

Fig. 5 is a diagram showing an arrangement of display pixels according to the present invention and the related art.

Fig. 6 is a diagram for explaining a method for calculating the emissive luminance ratio for R, G, and B when indicating white.

DESCRIPTION OF PREFERRED EMBODIMENT

A display device according to the present invention is described next described.

Fig. 1 is a plan view of an EL display device 100. In Fig. 1, portions corresponding to an EL element driving TFT for supplying a current to an EL element are shown enlarged.

The figure illustrates an example in which the display pixels emit either red (110R), green (110G), or blue (110b). Among the components of a display pixel shown in the above-described Fig. 3, the only components shown enlarged in Fig. 1 are the gates 41 and the active layer 43 including the channel 43c, source 43s, and drain 43d of a driving TFT 40 arranged within a display pixel.

As can be seen in Fig. 1, in the EL display device 100, a plurality of gate signal lines 51 are arranged in a horizontal

direction in the figure, and a plurality of power source lines 52 and drain signal lines 53 are arranged in a vertical direction in the figure. These lines 51 and 53, and 51 and 52, intersect one another.

5 As shown in Fig. 3, in an area near an intersection, a switching TFT 30 for controlling the timing for supplying a current to an EL element 60 is formed connected to the two signal lines 51,53. An EL element driving TFT 40 for supplying a current to the organic EL element 60 to drive the element is formed with its gate connected to the source 13s of the TFT 30. Further formed is the organic EL element 60 having an anode 61 connected to the source 43s of the EL element driving TFT 40. As shown in Fig. 1, display pixels for respective colors 110R, 110B, and 110G are arranged in a matrix.

10 The organic EL element 60 has a structure identical to the structure shown in Fig. 4B, and its explanation will not be repeated. The anode 61 is made of ITO, the hole transport layer 63 is composed of MTDATA (first hole-transport layer) and TPD (second hole-transport layer), the emissive layer 64 is composed of quinacridone, and the emissive element layer (electron transport layer) 65 is composed of Beq₂. The cathode 67 may be formed of an alloy of magnesium (Mg) and silver (Ag), or materials such as lithium fluoride (LiF). Chemical formulas for MTDATA, TPD, and Beq₂ are as follows.

MTDATA: 4,4',4''-tris(3-methylphenylphenylamino)triphenylamine

25 TPD: N,N'-diphenyl-N,N'-di(3-methylphenyl)-1,1'-biphenyl-4,4'-diamine

Bebq₂: bis(10-hydroxybenzo[h]quinolinato)beryllium

Transistor size W/L of an EL element driving TFT connected to each of the display pixels 110R, 110B, 110G is next explained.

In the present invention, transistor size of a TFT refers to the ratio of the channel width W to the channel length L in the TFT channel, namely, W/L.

In the present embodiment, the emissive material disposed in a green display pixel has the highest emissive efficiency. The emissive material disposed in a red display pixel has the second highest emissive efficiency, and the emissive material used in a blue display pixel has the lowest emissive efficiency. More specifically, the present embodiment refers to a case in which the ratio among the green emissive efficiency G_{eff} , the red emissive efficiency R_{eff} , and the blue emissive efficiency B_{eff} can be expressed as $G_{eff} : R_{eff} : B_{eff} = 10 : 3.8 : 1.8$.

L of the transistor size W/L in an EL element driving TFT in Fig. 1 equals to $L_1 + L_2$.

Among the transistor sizes (W/L) of driving TFTs in all display pixels, the smallest W/L is used to form the TFT of a green display pixel 110G because the emissive efficiency of the emissive material used for the emissive element layer of a green display pixel is the highest.

The W/L of display pixels for other colors 110R, 110B is larger than the W/L of a green display pixel 110G.

W/L may be varied by changing L while W is fixed, by changing W while L is fixed, or by changing both W and L.

Specifically, as the ratio between emissive efficiencies of respective colors is $G_{eff} : R_{eff} : B_{eff} = 10 : 3.8 : 1.8$, when the channel

length L in each TFT is fixed at 5μm, the TFT channel width of a green display pixel W_G may be designated at 5μm, the TFT channel width of a red display pixel W_R at 13μm, and the TFT channel width of a blue display pixel W_B at 28μm. This example is diagrammatically shown in Figs. 1A~1C.

More specifically, $W_G : W_R : W_B = 1 / G_{eff} : 1 / R_{eff} : 1 / B_{eff} = 1 / 10 : 1 / 3.8 : 1 / 1.8 = 1 : 2.6 : 5.6 = 5 : 13 : 28$.

By designating W/L according to the emissive efficiency of each color as described above, it is no longer necessary to adjust the current value of the EL driving power supply before supplying for each color, or to adjust, according to each color, the drain signal from the drain signal line which controls the voltage supplied from the source of the switching TFT 30 to the gate of the EL driving TFT 40. When the voltages need not be adjusted, it is no longer necessary to arrange the power source lines in a complex manner within the display region of the EL display device, eliminating causes of line breakage and short-circuits. In addition, it is no longer necessary to provide a separate circuit for controlling the voltage supplied to the drain of the switching TFT to control the voltage supplied to the gate of the EL element driving TFT.

While Figs. 1A~1C illustrate a case in which the channel length L, among the channel width W and the channel length L, is a fixed value, the present invention is not limited by this feature. The channel width W may be fixed while varying the channel length L in the display pixels for different colors. More specifically, as shown in Figs. 1D~1E, the channel length L of a green display

pixel 110G may be designated at the largest value, the channel length L of a red display pixel 110R at the next largest value to that of a green display pixel, and the channel length L of a blue display pixel 110B at the smallest value.

5 According to the present embodiment as described above, W/L of an EL driving TFT connected to a display pixel for each color is varied depending on the emissive efficiency of the emissive material of the emissive element layer disposed in that display pixel. This arrangement allows a voltage from the power source
10 to be altered for each color, eliminating the need to arrange power source lines in a complex manner within the display region of the EL display device. Further, circuit configuration is prevented from becoming complex by eliminating the need to alter a drain signal supplied to a switching TFT 30 according to each color.

15 The present embodiment illustrates a case in which, with the emitted colors being red, green, and blue, the smallest value is assigned for the transistor size W/L of an EL driving TFT connected to a green display pixel having the highest emissive efficiency, a larger value is assigned for the W/L of an EL driving TFT connected
20 to a red display pixel having the second highest emissive efficiency, and the largest value is assigned for the W/L of an EL driving TFT connected to a blue display pixel having the lowest emissive efficiency. However, the emissive efficiency of each color is changed depending on the emissive material used in the emissive
25 layer. Accordingly, the advantages of the present invention can be realized when display pixels for the color having the highest emissive efficiency (for example, green) and the second highest

emissive efficiency (which may be red) have the same W/L, while a display pixel for only the remaining color (blue) has a larger W/L. Alternatively, display pixels for blue having the lowest emissive efficiency and red having the next lowest emissive efficiency may have the same W/L, while a display pixel for the remaining color having the highest emissive efficiency, which may be green, has a smaller W/L.

To achieve an appropriate white balance of an indication, the value of current flowing in the TFT for each color is typically adjusted according to the chromaticity of the emissive material for each color, other than according to the emissive efficiency of the material. The transistor size may therefore be adjusted according to chromaticity.

More specifically, the target white color may be denoted by the chromaticity coordinates (x,y:0.31, 0.32) of the standard white light source (C light source) by NTSC scheme. When the chromaticity of R, G, and B light emitted from the organic EL elements is expressed by the coordinates indicated in the upper row of Fig. 6, luminance required for each of R, G, and B to achieve the white color at 100% luminance is determined as, for example, 25%:46%:29%. Denoting this by luminance ratio, R:G:B = 0.54 : 1 : 0.63.

When the ratio of emissive efficiencies among different colors of organic EL elements is, as in the above example, 10 : 3.8 : 1.8 for green, red, and blue, the ratio of necessary current amount to accomplish the luminance ratio of G:R:B = 1 : 0.54 : 0.63 is $G:R:B = 1/10 : 0.54/3.8 : 0.63/1.8 = 1 : 14.2 : 35$.

The sizes of the driving TFTs for supplying current to the organic EL elements for R, G, and B can be determined such that the ratio of the current amounts for R, G, and B equals to the above example ratio by taking into account the chromaticity of R, G, and B, the chromaticity of the target white color, and the emissive efficiency for each color. Simply by selecting such transistor sizes, the white color at 100% luminance can be achieved requiring no further arrangements.

In an EL display device in which the ratio among transistor sizes of the driving TFTs are determined according to the above method, luminance balance among different colors is already adjusted by the transistor sizes. Accordingly, control of white balance is extremely facilitated. Further, it is no longer necessary to apply a high voltage to the driving TFTs of EL elements for a particular color to indicate white, allowing improvement of life of the overall display device.

When different materials are used, the chromaticity coordinate values of the R, G, and B lights emitted by the organic EL elements are changed, which in turn alters the luminance ratio among R, G, and B. In addition, the emissive efficiencies would also differ. Accordingly, the transistor sizes should be determined according to these variable factors, and should not be limited by the numeric values of the above example.

While an organic EL display device is used as an example in the above embodiment, the present invention is not limited to an organic EL display device. The present invention may be implemented in an inorganic EL display device comprising, instead

of an organic EL element, an inorganic EL element using an inorganic emissive material as the emissive element. Alternatively, the present invention may be implemented in a display device having a fluorescent layer between two electrodes as the emissive layer.

5 Furthermore, although the present embodiment illustrates a display device having a stripe arrangement in which the display pixels for the same color are vertically aligned, the present invention may be implemented in a display device having a delta arrangement to realize the same advantages.

10 In the present embodiment, to achieve color indication by emitting lights of R, G, and B in a plurality of display pixels arranged in a matrix on a substrate as shown in Fig. 9, different materials are used in organic EL elements 160 for the respective colors of red, green, and blue. A differing material is used for
15 each color especially concerning the organic compound having emissive function.

For example, in a red organic EL element 160R, ITO is used for the anode 161, MTDATA (first hole transport layer) and TPD (second hole transport layer) for the hole transport layer 162, Beq₂ doped with 2% ZnPr for the emissive layer 163, and a MgIn alloy for the cathode 166.
20

In a green organic EL element 160G, ITO is used for the anode 161, MTDATA (first hole transport layer) and TPD (second hole transport layer) for the hole transport layer 162, Beq₂ for the
25 emissive layer 163, and a MgIn alloy for the cathode 166.

In a blue organic EL element 160B, ITO is used for the anode 161, MTDATA (first hole transport layer) and TPD (second hole

transport layer) for the hole transport layer 162, 1AZM-Hex for the emissive layer 163, and a MgIn alloy for the cathode 166. Other than the above, the combination comprising ITO (anode), MTDATA (first hole transport layer) and TPD (second hole transport layer),
5 OXD-8 (emissive layer), and MgIn alloy (cathode) can be employed in a blue organic EL element 160B.

The official names for the above-mentioned chemical abbreviations are as follows.

ZnPr: 5,10,15,20-tetraphenylporphyrinato zinc

10 MTDATA: 4,4',4''-tris(3-methylphenylphenylamino)triphenylamine

TPD: N,N'-diphenyl-N,N'-di(3-methylphenyl)-1,1'-biphenyl-4,4'-diamine

Bebq₂: bis(10-hydroxybenzo[h]quinolinato)beryllium

1AZM-Hex: (N,N'-disalicylidene-1,6-hexanediaminato)zinc

15 OXD-8: 3-bis[5-p-dimethylaminophenyl)-1,3,4-oxadiazol-2-yl]benzene

Using these materials in the organic EL elements 160 for R, G, and B (160R, 160G, 160B), the emissive efficiencies obtained at the present are denoted as: element 160G > element 160R > element
20 160B. In an organic EL element 160, emissive luminance is dependent upon current (current density). Accordingly, to obtain equivalent luminance for each color by supplying an equivalent current to each display pixel, the transistor sizes of the driving TFTs should be designated, as described above, according to the
25 expression: TFT for green pixel < TFT for red pixel < TFT for blue pixel.

Although the EL element driving TFTs in the present embodiment

are described as having the so-called top gate type structure in which the gate electrodes are provided above the active layer with the gate insulating film disposed therebetween, the benefits of the present invention can similarly be achieved using a bottom gate type structure in which the gate electrodes are provided beneath the active layer with the gate insulating film disposed therebetween.

While the present embodiment is described by illustrating several pixels within an EL display device, the present invention can be applied to any desired number of display pixels such as VGA (640x480), SVGA (800x600), XGA (1024x768), or SXGA(1280x1024).

According to the present invention, there is provided an EL display device in which white balance among the display pixels for different colors can easily be controlled without requiring complex circuitry.

WHAT IS CLAIMED IS:

1. A color display device in which display pixels for indicating different colors are provided in plural numbers for each color and arranged in a matrix, said color display device comprising,
5 corresponding to each display pixel:

a self-emissive element for emitting light of a predetermined color; and

a driving thin film transistor (TFT) connected to said
10 self-emissive element for supplying a drive current to said self-emissive element; wherein

size of said driving TFT in a display pixel for one color is altered from that in a display pixel for another color.

2. The color display device as claimed in claim 1, comprising,
15 corresponding to each display pixel:

a switching TFT for controlling turning on and off of said driving TFT and a current therethrough.

3. The color display device as claimed in claim 1, wherein
20 said size of said driving TFT is determined according to an emissive efficiency of a self-emissive element connected to the driving TFT.

4. The color display device as claimed in claim 3, wherein
25 said size of a driving TFT connected to a self-emissive element having a high emissive efficiency is set smaller compared to said

size of a driving TFT connected to a self-emissive element having a low emissive efficiency.

5 5. The color display device as claimed in claim 3, wherein said size of a driving TFT connected to a self-emissive element having a highest emissive efficiency is set smaller compared to said size of a driving TFT connected to a self-emissive element having any other level of emissive efficiency.

10 6. The color display device as claimed in claim 5, wherein color of the self-emissive element having the highest emissive efficiency is green.

15 7. The color display device as claimed in claim 3, wherein said size of a driving TFT connected to a self-emissive element having a lowest emissive efficiency is set larger compared to said size of a driving TFT connected to a self-emissive element having any other level of emissive efficiency.

20 8. The color display device as claimed in claim 7, wherein color of the self-emissive element having the lowest emissive efficiency is either red or blue.

25 9. The color display device as claimed in claim 3, wherein said size of said driving TFT is made successively larger as the emissive efficiency decreases.

10. The color display device as claimed in claim 1, wherein
said self-emissive element is an electroluminescence
element.

5 11. The color display device as claimed in claim 1, wherein
said size of said driving TFT is altered by changing a gate
width according to emitting color while a gate length is fixed.

10 12. The color display device as claimed in claim 1, wherein
said size of said driving TFT is altered by changing a gate
length according to emitting color while a gate width is fixed.

13. A color display device in which a display pixels for
indicating different colors are provided in plural numbers for each
15 color and arranged in a matrix, said color display device comprising,
corresponding to each display pixel:

a self-emissive element for emitting light of a predetermined
color; and

20 a driving thin film transistor (TFT) connected to said
self-emissive element for supplying a drive current to said
self-emissive element; wherein

size of said driving TFT in a display pixel for one color is
set for every color in accordance with:

25 the emission efficiency of said emissive element disposed at
said display pixel;

the chromaticity of each color emitted by respective emissive
element; and

the chromaticity of target display white of the display device.

14. A color display device according to claim 13 wherein the size
5 of said driving TFT of the display pixel of any one color, among
the display pixel of various colors, is different from the size
of said driving TFT of the display pixel of another color.

15. A color display device according to claim 13 wherein said
10 emissive element is an organic electroluminescence element
comprising the emissive layer using an organic compound between
first electrode and second electrode.

16. A color display device in which display pixels for red, for
15 green, and for blue, are provided in plural numbers for each color
and arranged in a matrix, said color display device comprising,
corresponding to each display pixel:

a self-emissive element for emitting light of a predetermined
color; and

20 a driving thin film transistor (TFT) connected to said
self-emissive element for supplying a drive current to said
self-emissive element; wherein

size of said driving TFT in a display pixel for red, for green,
and for blue is set on the basis of:

25 the emission efficiency of the emissive element of each
display pixel; and

a luminance ratio of red to green to blue in accordance with

each chromaticity of red, green, and blue emitted by respective emissive element of the display pixel, and with the chromaticity of target display white of the display device.

5 17. A color display device according to claim 16 wherein the emissive area of the display pixel of any one color among said display pixel for red, for green, and for blue is different in size from the emissive area of the display pixel of another color.

10 18. A color display device according to claim 16 wherein said emissive element is an organic electroluminescence element comprising the emissive layer using an organic compound between first electrode and second electrode.

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[illegible]

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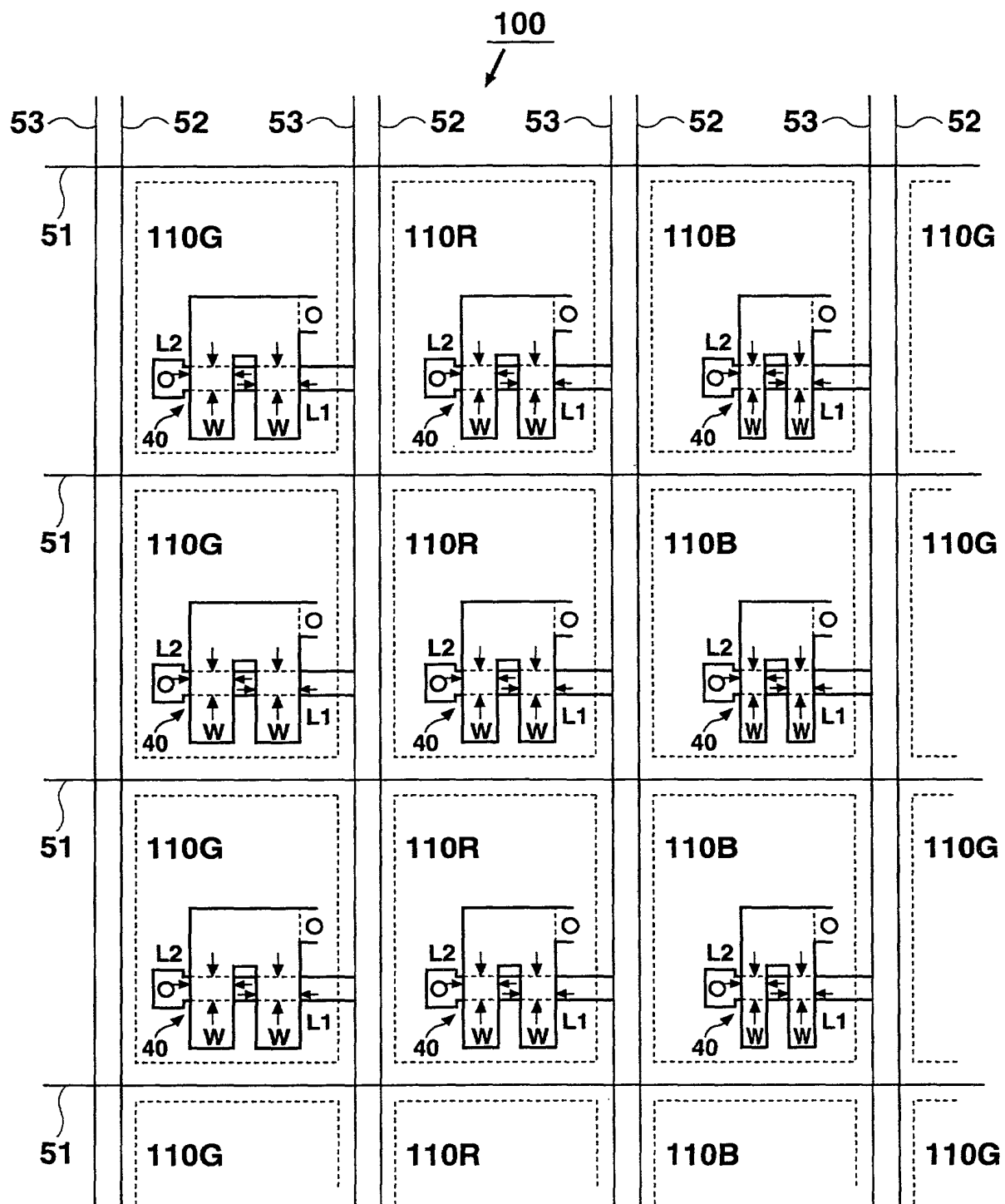


Fig. 1

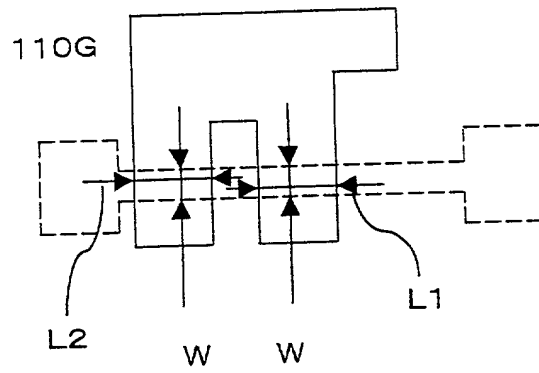


FIG. 1D

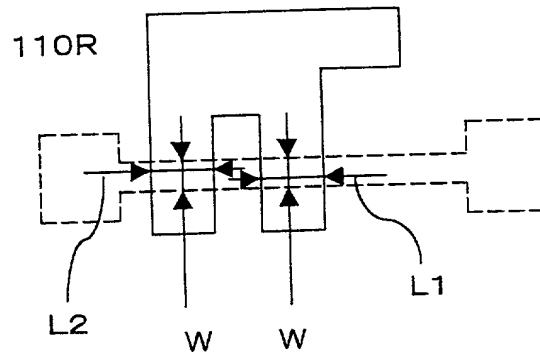


FIG. 1E

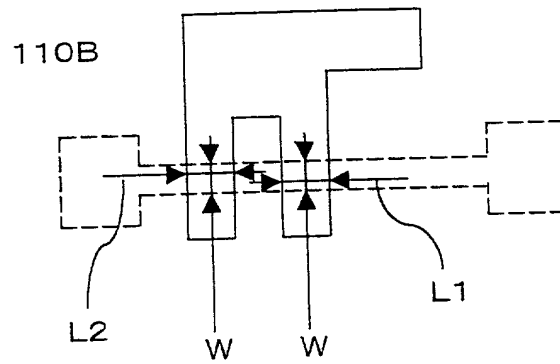


FIG. 1F

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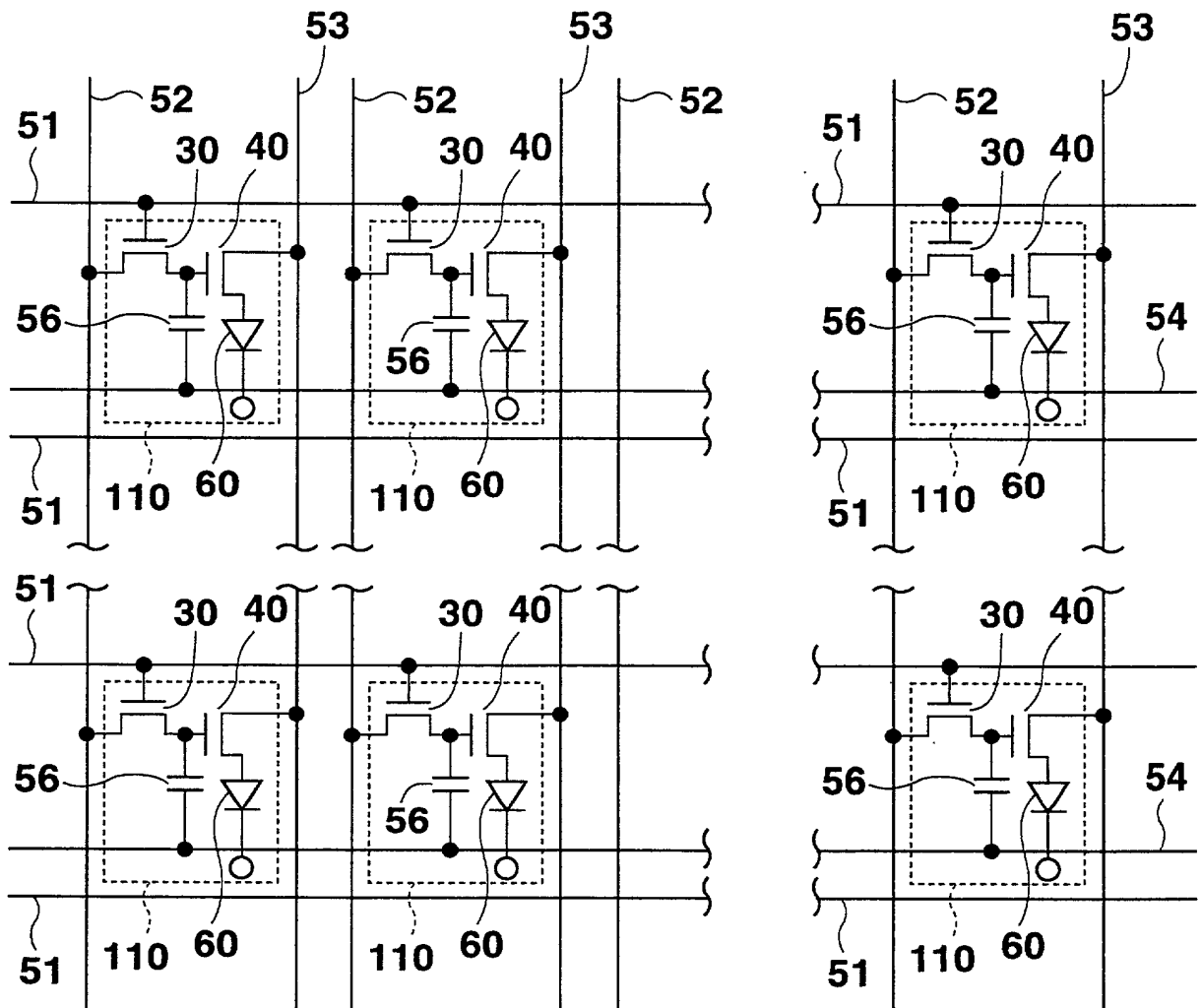


Fig. 2 RELATED ART

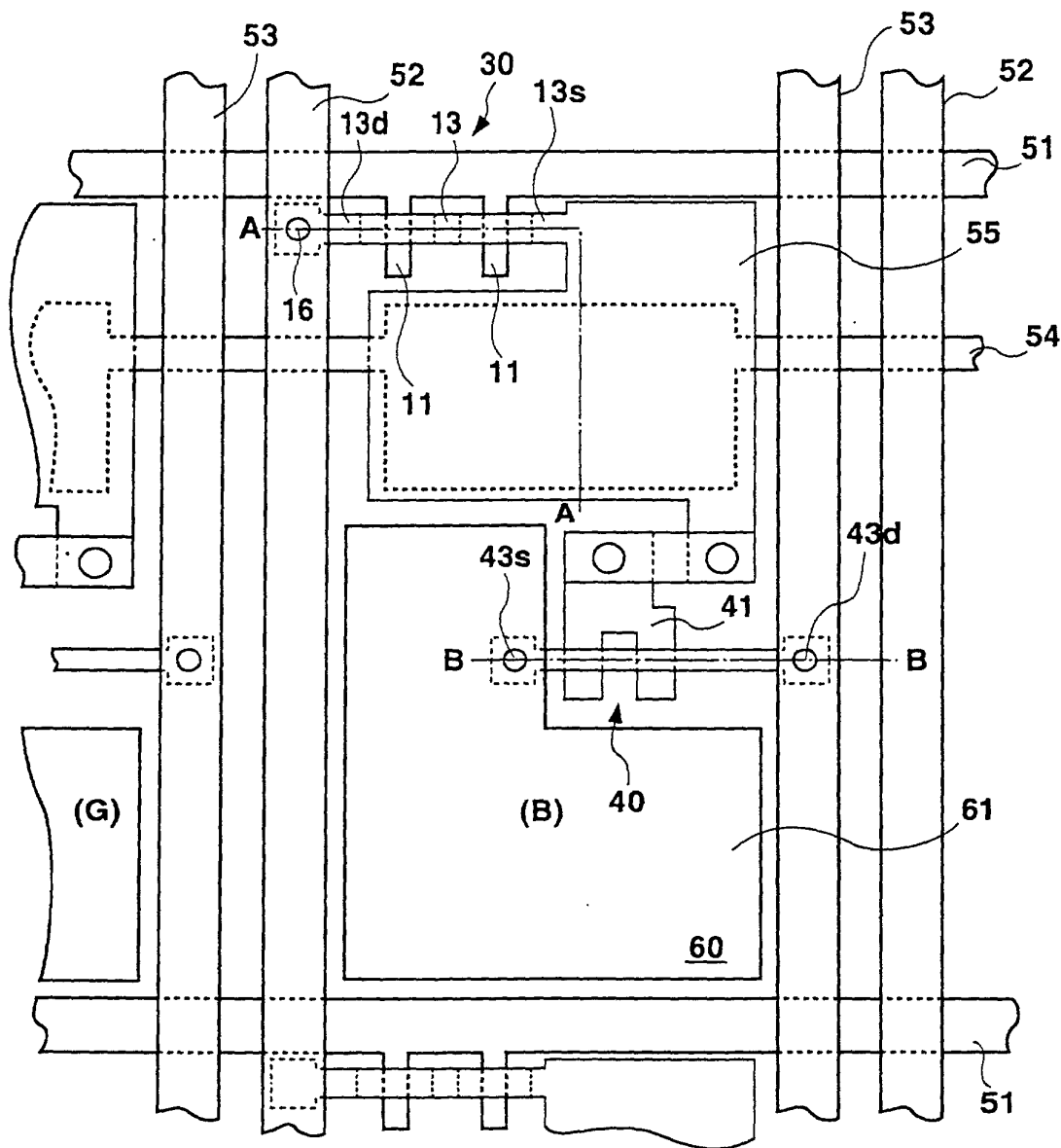


Fig. 3 RELATED ART

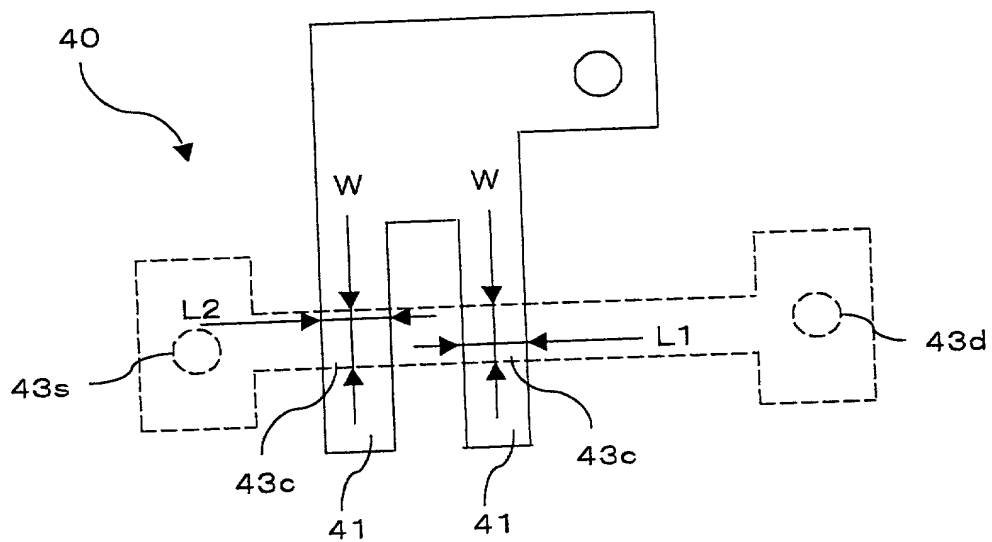


Fig. 3A **RELATED ART**

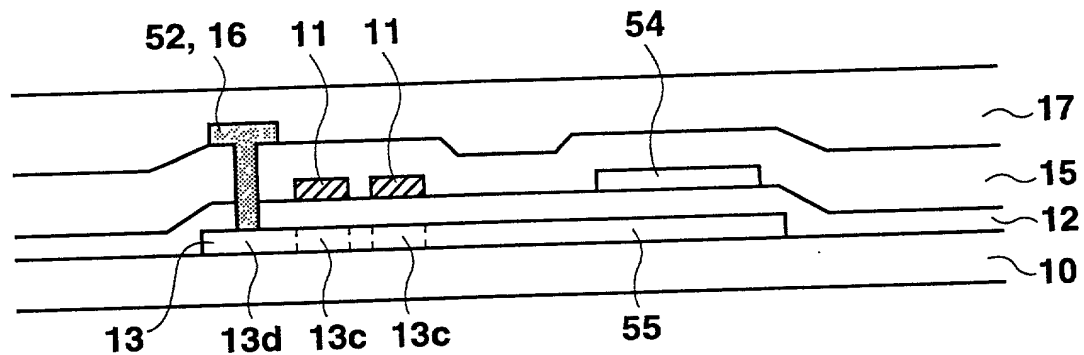


Fig. 4A RELATED ART

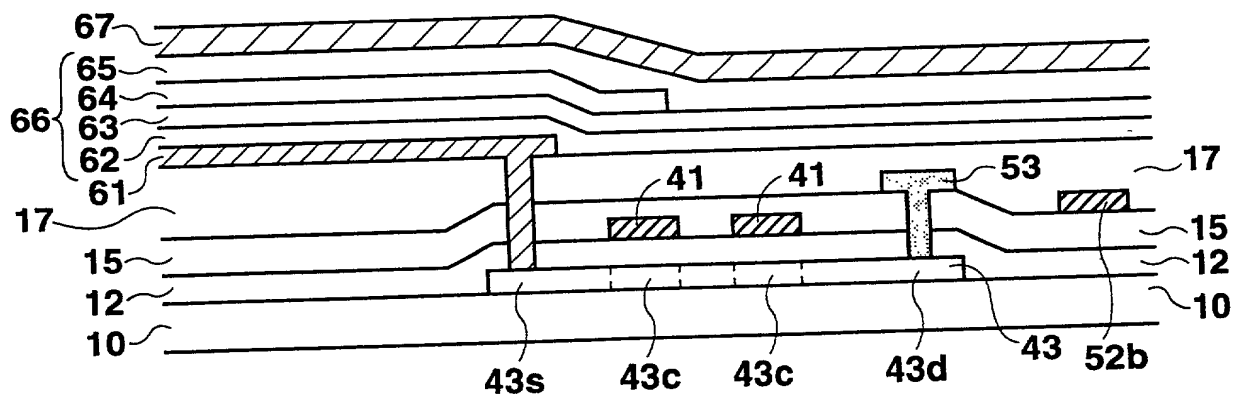


Fig. 4B RELATED ART

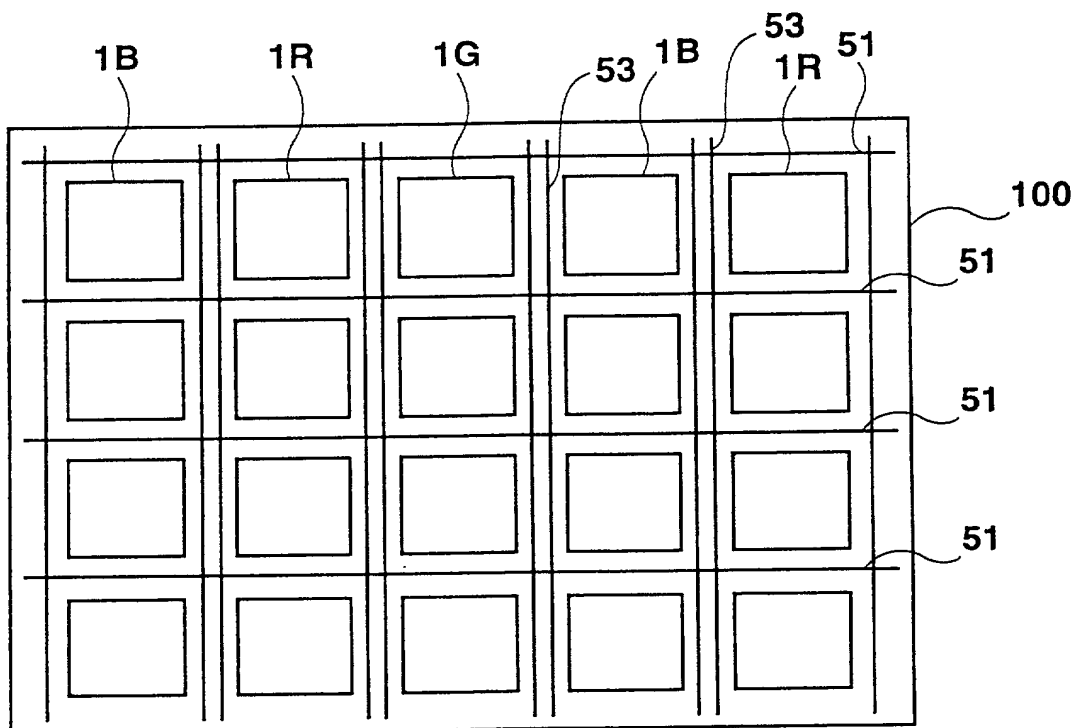


Fig. 5 **RELATED ART**

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	R		G		B		W	
CHROMATICITY COORDI- NATE	x	y	x	y	x	y	x	y
	0.65	0.34	0.30	0.63	0.17	0.17	0.31	0.32
LUMINANCE PERCENTAGE (WITH W AT 100%)	25(%)		46(%)		29(%)		100(%)	
LUMINANCE RATIO (G)	0.54		1		0.63		—	

Fig. 6

Declaration and Power of Attorney For Patent Application

特許出願宣言書及び委任状

Japanese Language Declaration

日本語宣言書

下記の氏名の発明者として、私は以下の通り宣言します。

As a below named inventor, I hereby declare that:

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

My residence, post office address and citizenship are as stated next to my name.

下記の名称の発明に関して請求範囲に記載され、特許出願している発明内容について、私が最初かつ唯一の発明者（下記の氏名が一つの場合）もしくは最初かつ共同発明者であると（下記の名称が複数の場合）信じています。

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

COLOR DISPLAY DEVICE

上記発明の明細書（下記の欄でX印がついていない場合は、本吉に添付）は、

the specification of which is attached hereto unless the following box is checked:

☐ 月 日に提出され、米国出願番号または特許協定条約国際出願番号を _____ とし、
(該当する場合) _____ に訂正されました。

☐ was filed on _____
as United States Application Number or
PCT International Application Number
_____ and was amended on
_____ (if applicable).

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則法典第37編第1条56項に定義されるとおり、特許資格の有無について真実な情報を開示する義務があることを認めます。

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

Japanese Language Declaration (日本語宣言書)

私は、米法法典第35編119条(a)-(d)項又は365条(b)項に基づき下記の、米国以外の国の少なくとも一ヶ国を指定している特許協力条約365(a)項に基づく国際出願、又は外国での特許出願もしくは発明者証の出願についての外国優先権をここに主張するとともに、優先権を主張している。本出願の前に出願された特許または発明者証の外国出願を以下に、枠内をマークすることで、示しています。

Prior Foreign Application(s)

外国での先行出願
Hei11-283181

(Number)
(番号)

Japan

(Country)
(国名)

04/10/1999

(Day/Month/Year Filed)
(出願年月日)

Priority Not Claimed
優先権主張なし

☐

(Number)
(番号)

(Country)
(国名)

(Day/Month/Year Filed)
(出願年月日)

☐

(Number)
(番号)

(Country)
(国名)

(Day/Month/Year Filed)
(出願年月日)

☐

(Number)
(番号)

(Country)
(国名)

(Day/Month/Year Filed)
(出願年月日)

☐

私は、第35編米国法典119条(e)項に基づいて下記の米国外特許出願規定に記載された権利をここに主張いたします。

(Application No.)
(出願番号)

(Filing Date)
(出願日)

(Application No.)
(出願番号)

(Filing Date)
(出願日)

私は、下記の米国法典第35編120条に基づいて下記の米国外特許出願に記載された権利、又は米国を指定している特許協力条約365条(c)に基づき権利をここに主張します。また、本出願の各請求範囲の内容が米国法典第35編112条第1項又は特許協力条約で規定された方法で先行する米国外特許出願に開示されていない限り、その先行米国外出願提出日以降で本出願書の日本国内または特許協力条約国際提出日までの期間中に入手された、連邦規則法典第37編1条56項で定義された特許資格の有無に関する重要な情報について開示義務があることを認識しています。

(Application No.)
(出願番号)

(Filing Date)
(出願日)

(Status: Patented, Pending, Abandoned)
(現況: 特許許可済、係属中、放棄済)

(Application No.)
(出願番号)

(Filing Date)
(出願日)

(Status: Patented, Pending, Abandoned)
(現況: 特許許可済、係属中、放棄済)

私は、私自身の知識に基づいて本宣言書中で私が行なう表明が真実であり、かつ私の入手した情報と私の信じているところに基づき表明が全て真実であると信じていること、さらに故意になされた虚偽の表明及びそれと同等の行為は米国法典第18編1001条に基づき、罰金または拘禁、もしくはその両方により処罰されること、そしてそのような故意による虚偽の表明を行えば、出願した、又は既に許可された特許の有効性が失われることを認識し、よってここに上記のごとく宣誓を致します。

I hereby claim foreign priority under Title 35, United States Code, Section 119 (a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below.

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or 365(c) of any PCT international application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of application.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

004007 20060900

(特刊) 圖書

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